

11/08/01
JCS25 U.S. PTO

12.7.01

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Docket No.: M-11914 US

November 8, 2001

Box Patent Application
Commissioner For Patents
Washington, D. C. 20231

J1050 U.S. PTO
10/005827
11/08/01

Enclosed herewith for filing is a patent application, as follows:

Inventor(s): Yoo, Woo Sik

Title: Gas-Assisted Rapid Thermal Processing

X Return-receipt Postcard
X This Transmittal Letter (in duplicate)
9 page(s) Specification(not including claims)
3 page(s) Claims
1 page Abstract
6 Sheet(s) of Drawings
2 page(s) Declaration For Patent Application and Power of Attorney
1 page(s) Recordation Form Cover Sheet (in duplicate)
1 page(s) Assignment
1 page(s) PTO-Form 1449 citing seven (7) references
1 Copy(ies) of seven (7) Cited References submitted



Applicant(s) assert(s) entitlement to small entity status for the attached patent application

CLAIMS AS FILED (fees computed under 37 CFR §1.9(f))

For	Number Filed		Number Extra		Rate		Basic Fee
Total Claims	12	-20 =	0	x	\$ 9.00	=	\$ 370.00
							\$ 0.00
Independent Claims	3	-3 =	0	x	\$42.00	=	\$ 0.00
<input type="checkbox"/>	Fee of _____ for the first filing of one or more multiple dependent claims per application						\$
<input type="checkbox"/>	Fee for Request for Extension of Time						\$

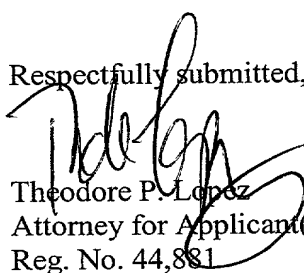
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Respectfully submitted,


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GAS-ASSISTED RAPID THERMAL PROCESSING

Woo Sik Yoo

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BACKGROUND

1. Field of the Invention

This invention generally relates to semiconductor manufacturing equipment and, more particularly, to an improved method for rapid thermal processing of a semiconductor wafer.

10 **2. Related Art**

In the semiconductor manufacturing industry, depending upon the particular process, a semiconductor wafer may be treated at temperatures of from about 100° C to about 1300° C, under controlled conditions, in rather sophisticated furnaces. Commonly, these furnaces are horizontal or vertical type furnaces, which use various energy sources to heat the wafer, including radiant heaters, arc lamps, and tungsten-halogen lamps. As shown in FIG. 1, a typical horizontal or vertical type furnace requires a time t_1 for ramping up to a particular process temperature to process the wafers. The ramp up rate for a typical furnace is usually between 5 °C/min to about 15 °C/min, which makes time t_1 typically on the order of about 1 hour. A time t_2 is required for cooling of the wafers, which is generally on the order of about 2 hours. Long processing times are typically unacceptable in advanced semiconductor device manufacturing because of dopant redistribution, excessive costs, excessive exposure to temperature, and high power requirements.

25 In order to continue to make advancements in the development of semiconductor devices, especially semiconductor devices of decreased dimensions, new processing and manufacturing techniques have been developed. One such processing technique is know as Rapid Thermal Processing (RTP), which reduces the amount of time that a semiconductor device is exposed to high
30 temperatures during processing. The rapid thermal processing technique, typically includes raising the temperature of the wafer and holding it at that

temperature for a time long enough to successfully perform a fabrication process, and avoid such problems as unwanted dopant diffusion that would otherwise occur at the high processing temperatures.

5

SUMMARY

The present invention provides an improved system, method and structure for rapid thermal processing of a semiconductor wafer. The present invention helps to improve process times so as to avoid, for example, the creation of thermal gradients in the process chamber, which can cause slip and warpage of the wafer.

10 The present invention adds a significant conductive heat transfer component to processes which may presently use primarily radiant heat transfer to raise the temperature of a semiconductor wafer.

In accordance with the present invention, a gas, such as He, H₂, O₂, Ar, N₂, and gases containing He, H₂, O₂, Ar and N₂, can be introduced into the processing
15 chamber during processing. The gas is introduced through at least one heatable member, such as a heatable plate that can overlay a surface of the wafer. The heatable member includes an internal cavity in which heat is transferred to the gas. The heated gas exits the heatable member in the direction of the wafer surface through an outlet portion of the plate. The outlet portion can include a plurality of
20 holes, which are dispersed on the plate surface so as to evenly distribute the heated gas across the surface of the wafer. Advantageously, the addition of the heated gas to the process can raise the temperature of the wafer much more quickly than without the use of the gas.

These and other features and advantages of the present invention will be
25 more readily apparent from the detailed description of the preferred embodiments set forth below taken in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE FIGURES

FIG. 1 is a graph representative of typical temperature heat-up and cool-down rates for a conventional wafer processing furnace;

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FIG. 2 illustrates schematically a side view of one embodiment of a semiconductor wafer processing system in accordance with the present invention;

FIG. 3A is a simplified cross-sectional illustration of an embodiment of a processing chamber in accordance with the present invention;

FIG. 3B is a simplified illustration of a side view of a heating assembly in accordance with the present invention;

5 FIG. 4A is a top view of a heatable plate including standoffs for holding a wafer in accordance with an embodiment of the present invention;

FIG. 4B is a cross-sectional view of the heatable plate of FIG. 4A;

FIG. 5 is a simplified cross-sectional illustration of an embodiment with directional gas flow indications in accordance with the present invention;

10 FIG. 6 is a graph comparing wafer temperature as a function of time between a wafer disposed in the process chamber of FIG. 3 without gas flow and a wafer disposed in the process chamber of FIG. 3 with gas flow in accordance with principles of the present invention;

FIG. 7 illustrates an alternative embodiment of the process chamber of FIG. 3 used for cooling a wafer; and

15 FIG. 8 illustrates yet another alternative embodiment of the process chamber of FIG. 3 used for cooling a wafer.

DETAILED DESCRIPTION

20 FIGS. 2 illustrates schematically a side view of one embodiment of a semiconductor wafer processing system 10 that establishes a representative environment of the present invention. Processing system 10 includes a loading station 12, a transfer chamber 20 and a reactor 26. A robot 22 within transfer chamber 20 rotates toward loadlock 18 and picks up a wafer 24 from cassette 16. Reactor 26, which may also be at atmospheric pressure or under vacuum pressure, accepts wafer 24 from robot 22. Robot 22 then retracts its arm which carried wafer 24 and, subsequently, the processing of wafer 24 begins.

25 Reactor 26 may be any type of reactor which allows wafers to be loaded at wafer processing temperatures, of between about 100°C to about 1300°C, without adverse results. In accordance with the present invention, the reactor may be a hot-walled RTP reactor, such as is used in thermal anneals. In other embodiments, the reactor may be the type of reactor used for dopant diffusion, thermal oxidation, nitridation, chemical vapor deposition, and/or similar processes. In one embodiment, process gases, coolant, and

electrical connections may be provided through the rear end of the reactors using interfaces 34.

FIG. 3A is a simplified cross-sectional illustration of an embodiment of a reactor 100 in accordance with the present invention. Reactor 100 may generally include a closed-end process chamber 106, which defines an interior cavity 108. Process chamber 106 may be constructed with a substantially rectangular cross-section, having a minimal internal volume surrounding wafer 110. In one embodiment, the volume of process chamber 106 may be no greater than about 5000 cm³, preferably less than about 3000 cm³. One result of the small volume is that uniformity in temperature is more easily maintained. Additionally, the small process chamber volume allows reactor 100 to be made smaller, and as a result, processing system 10 may be made smaller, requiring less clean room floor space. Process chamber 106 may be made of aluminum, quartz or other suitable material, such as silicon carbide or Al₂O₃. To conduct a process, process chamber 106 should be capable of being pressurized. Typically, process chamber 106 should be able to withstand internal pressures of about 0.001 Torr to 1000 Torr, preferably between about 0.1 Torr and about 760 Torr.

Opening 104, shown at the left end of process chamber 106, provides access for the loading and unloading of wafer 110 before and after processing. Opening 104 may be a relatively small opening, but with a height and width large enough to accommodate a wafer of between about 0.5 to 2 mm thick and up to about 300 mm (~12 in.) in diameter, and the arm of robot 22 passing therethrough. The height of opening 104 is no greater than between about 18 mm and 50 mm, and preferably, no greater than 20 mm. The relatively small opening size helps to reduce radiation heat loss from process chamber 106, and keeps down the number of particles entering cavity 108 to allow for easier maintenance of the isothermal temperature environment.

In one embodiment, a plurality of heating elements 114 surround a top and a bottom portion of process chamber 106. Resistive heating elements 114 may be disposed in parallel across chamber 106 such that each element 114 is in relative close proximity to each other element 114. For example, each resistive heating element 114 may be spaced between about 5 mm and 50 mm apart; preferably between about 10 mm and 20 mm apart. Accordingly, the close spacing of heating elements 114 provides for an even heating temperature distribution across the wafer positioned in cavity 108. Resistive heating element 114 may include a

resistive heating element core and a filament wire. The core is usually made of a ceramic material, but may be made of any high temperature rated, non-conductive material. The filament wire is conventionally wrapped around the core to allow an optimal amount of heat energy to radiate from the element. The filament wire may be any suitable resistively heatable wire, which is made from a high mass material for increased thermal response and high temperature stability, such as SiC, SiC coated graphite, graphite, NiCr, AlNi and other alloys. Preferably, the resistive heating filament wire is made of a combination Al-Ni-Fe material, known commonly as Kantal A-1 or AF, available from Omega Corp. of Stamford, Connecticut.

A direct line voltage of between about 100 volts and about 500 volts may be used to power the resistive elements. Thus, in this embodiment no complex power transformer is needed for controlling the output of resistive heating elements 114.

In one embodiment, reactor 100 includes heat diffusing members 118 and 120, which are positioned proximate to and typically overlay heating elements 114. Heat diffusing members 118 and 120 absorb the thermal energy output from heating elements 114 and dissipate the heat evenly within chamber 106. It should be appreciated that by heating wafer 110 from above and below, and further by keeping the distance between heat diffusing members 118 and 120 small, the temperature gradient within chamber 106 is more easily isothermally maintained. Heat diffusing members 118 and 120 may be any suitable heat diffusing material that has a sufficiently high thermal conductivity, preferably Silicon Carbide, Al_2O_3 , or graphite.

As further illustrated in FIGS. 3A and 3B, in one embodiment, reactor 100 includes a heating assembly 122. Heating assembly 122 includes two heatable members or top plate 124 and bottom plate 126 positioned, as shown in FIG. 3, adjacent and opposed to one another. Top plate 124 is spaced apart from bottom plate 126 a distance ϕ which allows wafer 110 to be placed therebetween. For example, the distance ϕ between plates 124 and 126 can be between about 10 mm to about 50 mm.

Heating plates 124 and 126 of heating assembly 122 may have a large mass relative to wafer 110, and may be fabricated from a material, such as silicon

carbide, quartz, Inconel, aluminum, steel, or any other material that does not react at processing temperatures with any ambient gases or with wafer 110.

Arranged on a top surface of bottom plate 126 may be wafer supports 112. In one embodiment, wafer supports 112 extend outward from the surface of bottom plate 126 to support wafer 110. Wafer supports 112 are sized to ensure that wafer 110 is held in close proximity to bottom plate 126. For example, wafer supports 112 may each have a height of between about 50 μm and about 20 mm, preferably about 2 mm to about 8 mm. The present invention includes at least three wafer supports 112 to ensure stability. However, the total area of contact between wafer supports 112 and wafer 110 is less than the size of the wafer.

Top and bottom plates 124 and 126 may be formed into any geometric shape, preferably a shape which resembles that of the wafer. In one embodiment, plates 124 and 126 are circular plates. The dimensions of plates 124 and 126 may be larger than the dimensions of wafer 110, such that the surface area of wafer 110 can be completely overlaid by the surface area of heating plates 124 and 126. In one example, the circular diameters of heating plates 124 and 126 is greater than the diameter of wafer 110. For example, as illustrated in FIG. 4A, the radius of bottom heating plate 126 is greater than the radius of wafer 110 by about a length γ , which can range between about 1 mm and 100 mm, preferably 25-to-50 mm.

For convenience of understanding, FIG. 4A illustrates an embodiment of a heatable member, for example, top plate 124. It should be understood that top plate 124 is similar in structure and function to bottom plate 126, and the description of top plate 124 is the same as for bottom plate 126, unless otherwise noted. In accordance with one embodiment of the present invention, on a periphery of heating top plate 124 is at least one heat source 222. Heat source 222 may be a resistive heating element or other conductive heat source, which can be made to contact a peripheral portion of top heating plate 124 or may be embedded within top heating plate 124. The resistive heating element may be made of any high temperature rated material, such as a suitable resistively heatable wire, which is made from a high mass material for increased thermal response and high temperature stability, such as SiC, SiC coated graphite, graphite, AlCr, AlNi and other alloys. Resistive heating elements of this type are available from Omega

Corp. of Stamford, Connecticut. Alternatively, top heating plate 124 can be heated using a radiant source.

In one embodiment, plates 124 and 126 may be positioned suspended within process chamber 106, in a cantilevered relationship on a wall of the process chamber. In this embodiment, coupling mechanism 224 includes a mounting bracket 228 and electrical leads 230 to provide an electrical power connection to heat source 222. Mounting bracket 228 can be coupled to an internal wall of process chamber 106 using conventional mounting techniques. Once mounted, electrical leads 230 can extend outside of process chamber 106 to be connected to an appropriate power source. The power source may be a direct line voltage of between about 100 volts and about 500 volts. Alternatively, top plate 124 may hang suspended from mounts emanating down from a ceiling of process chamber 106 and bottom plate 126 may rest on mounts emanating up from a floor of process chamber 106.

FIG. 4B is a simplified cross sectional view of heating plate 124, 126, in accordance with the present invention. Heating plates 124, 126 can include a hollowed out portion defining an internal cavity 250. Internal cavity 250 has an inlet 252, which enters internal cavity 250 on a first side 256 of plate 124, 126. Inlet 252 allows a gas to be fed from a gas reservoir (not shown) into internal cavity 250. The gas may include, for example, any suitable gas, such as He, H₂, O₂, Ar, N₂ and the like.

Internal cavity 250 has an outlet portion 254, which is defined on a second side 258 of plate 124, 126. Outlet portion 254 allows the gas that has entered internal cavity 250 to escape the cavity. In one embodiment, outlet portion 254 can be defined as a plurality of outlets or holes 255. Holes 255 can be any suitable size that allow passage of the gas. In one example, holes 255 may be between about 0.1 mm to about 2 mm in diameter.

In one embodiment, internal cavity 250 may include a baffle 260, positioned between inlet 252 and outlets 255. In this embodiment, baffle 260 impedes the flow of the gas entering through inlet 252. By impeding the otherwise forced gas flow before exiting through outlets 255, the gas is made to reside in internal cavity 250 for a longer time period, which allows more heat to be transferred to the gas before the gas exits the cavity.

In one embodiment, the thickness of top and bottom plates 124 and 126 is any thickness d suitable for accommodating heat source 222 and for providing an adequately sized internal cavity 250. The thickness d can range from between 1 cm and 10 cm; preferably about 3 cm. In one example, plates 124 and 126 can be fabricated using a split construction, which means that each plate is originally two pieces 263 and 267, joined at seam 265, for example, by welding. Baffle 260 can be mounted onto piece 267, for example, using attaching device 261, such as screws, nuts and bolts, rivets and the like and a spacer 269. It should be noted that one of ordinary skill in the art may fabricate plates 124 and 126 using a variety of well known techniques, all of which fall within the scope of the present invention. Generally, plates 124 and 126 may be made of materials, such as Al, and Al alloys Ni, Inconel, Mo, stainless steel, SiC and the like.

Referring now to FIGS. 4B, 5 and 6, in operation, internal cavity 250 serves as a heat exchanger, such that the gas can be heated as it travels from inlet 252 through to the exit points of outlets 254. The entering gas can be at ambient temperature or may be pre-heated prior to entering internal cavity 250. The gas is made to move through internal cavity 250 which may be heated by heat source 222 to between approximately 100°C and 1000°C. It should be understood that the temperature of the gas exiting internal cavity 250 depends on, for example, the type of gas, the flow rate of the gas, the residence time of the gas in internal cavity 250 and the nominal temperature of internal cavity 250. Each of these parameters can be adjusted until the exiting gas temperature is appropriate for a specific process.

As shown in FIG. 5, in one embodiment, wafer 110 is placed between top and bottom plates 124 and 126 and a gas, such as N_2 , is allowed to flow through internal cavity 250 of top plate 124. In this example, the gas enters internal cavity 250 at approximately room temperature (25 °C). The gas is heated by radiation and conduction as it passes through cavity 250 and exits outlets 255.

FIG. 6 is a graph showing an exemplary result of a heating process using the configuration of FIG. 5. In this example, heat source 222 is set to about 220°C, which causes the gas to exit outlets 255 at approximately 215°C.

As illustrated by line 264 in FIG. 6, allowing gas to flow through top plate 124 only, wafer 110 reaches the temperature of approximately 220 °C in about 60 seconds. Line 266 illustrates approximately the same heating profile if

gas is allowed to simultaneously flow through bottom plate 126. For comparison, line 262 represents the heating profile of wafer 110 without gas flow through either top plate 124 or bottom plate 126. As illustrated by line 262, approximately 270 seconds are required to first heat wafer 110 to approximately 210 °C.

FIG. 7 is a simplified illustration of another embodiment in accordance with the present invention. In this embodiment, process chamber 106 is structurally the same as in other embodiments, including top plate 124 and bottom plate 126. However, instead of heating the gas as it enters chamber 106, gas is removed from chamber 106, through top plate 124 and bottom plate 126 to provide forced convection cooling of wafer 110. In operation, when cooling is desired, a vacuum is pulled through inlets 252 to cause gas to flow out from chamber 106 in a direction generally indicated by air flow arrows 270 and into outlets 255. In this embodiment, outlet portion 254 becomes an inlet to internal cavity 250 and gas inlet 252 provides an outlet for internal cavity 250.

In one embodiment, an exhaust port 272 in chamber 106 can be opened to provide ambient air for cooling. As the ambient air is moved over the surface of wafer 110, the wafer surface is cooled. In one embodiment, the cooling rate can range from between approximately 5°C/sec and 20°C/s.

FIG. 8 shows another embodiment for moving gas over the surface of wafer 110. In this embodiment, a gas inlet 280 is positioned on chamber 106 approximately in-line and parallel to wafer 110. An outlet or exhaust port 282 is also positioned in-line and parallel to wafer 110, and approximately in-line with inlet 280. In operation as a gas is allowed into chamber 106 through inlet 280, outlet port 282, under vacuum, causes the gas to flow over wafer 110 providing a force convection cooling effect.

Having thus described embodiments of the present invention, persons skilled in the art will recognize that changes may be made in form and detail without departing from the spirit and scope of the invention. Thus the invention is limited only by the following claims.

WHAT IS CLAIMED IS:

1. A system for processing a semiconductor device, the system comprising:

a processing chamber; and

5 a first plate positioned within said processing chamber and defining a first internal cavity configured to receive a first gas through a first passage into said first internal cavity at a first temperature and to emit said first gas from said first internal cavity at a second temperature through a second passage.

10 2. The system of Claim 1, further comprising a second plate disposed adjacent to said first plate, wherein said second plate defines a second internal cavity configured to receive a second gas through a first passage into said second internal cavity at a first temperature and to emit said gas from said second internal cavity at a second temperature through a second passage.

15 3. The system of Claim 2, wherein said second passages comprise a plurality of holes defined on a surface of said first and said second plates.

20 4. The system of Claim 2, wherein said first plate and said second plate comprises a heat source for heating said plate to a preselected temperature.

25 5. The system of Claim 1, wherein said first gas is taken from the group consisting of N₂, He, H₂, O₂, Ar and gas mixtures containing He, H₂, O₂, Ar and N₂.

6. The system of Claim 1, wherein said internal cavity further comprises a buffer to disperse said first gas throughout said internal cavity.

7. A system for wafer processing comprising:

a chamber; and

at least one heatable plate positionable within said chamber, including:

an internal cavity defining an internal wall and configured to

5 receive a gas;

means for heating said internal wall to a preselected

temperature; and

an outlet portion defining a plurality of holes for emitting said gas.

10 8. The system of Claim 7, wherein said at least one heatable plate comprises a first heatable plate and a second heatable plate disposed having adjacent surfaces configured to receive a wafer therebetween.

15 9. The system of Claim 7, wherein said gas is taken from the group consisting of He, H₂, O₂, Ar, N₂ and gas mixtures containing He, H₂, O₂, Ar, and N₂.

20 10. The system of Claim 7, wherein said internal cavity further comprises a buffer to disperse said first gas throughout said internal cavity.

11. A method for processing a semiconductor device, the method comprising:

providing a first heatable member including:

an internal cavity defining an internal wall;

25 means for heating said internal wall to a preselected

temperature; and

an outlet portion defining a plurality of holes;

introducing a gas into said internal cavity of said first heatable member;

heating said gas substantially to said preselected temperature; and

impinging a surface of a semiconductor wafer with said heated gas to
change the temperature of said semiconductor wafer.

12. The method of Claim 11, wherein heating said gas substantially to
5 said preselected temperature comprises heating said internal wall with a resistance
heating element to said preselected temperature.

GAS-ASSISTED RAPID THERMAL PROCESSING

Woo Sik Yoo

ABSTRACT

5 A system, method and apparatus for processing a semiconductor device
including a processing chamber and a heating assembly positioned within the
processing chamber. The heating assembly including at least a plate defining an
internal cavity configured to receive gas. The gas enters the internal cavity
through a first passage at a first temperature, and exits the internal cavity at a
10 second temperature through a second passage.

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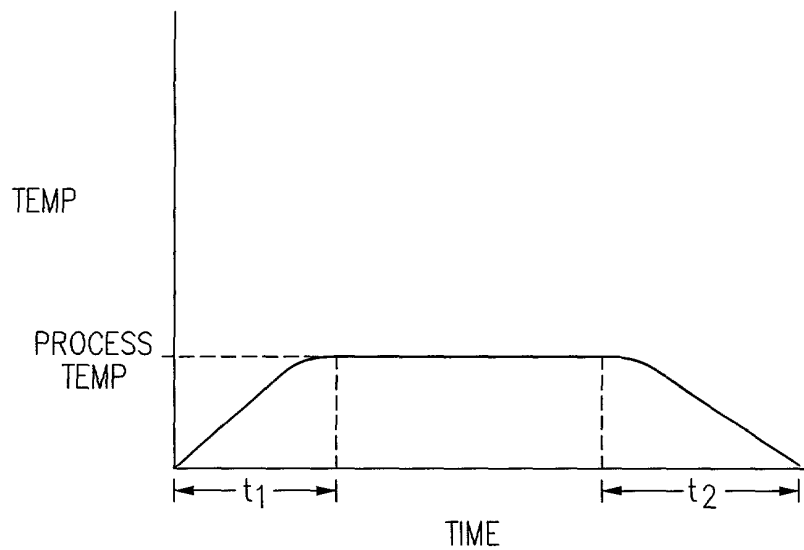


FIG. 1

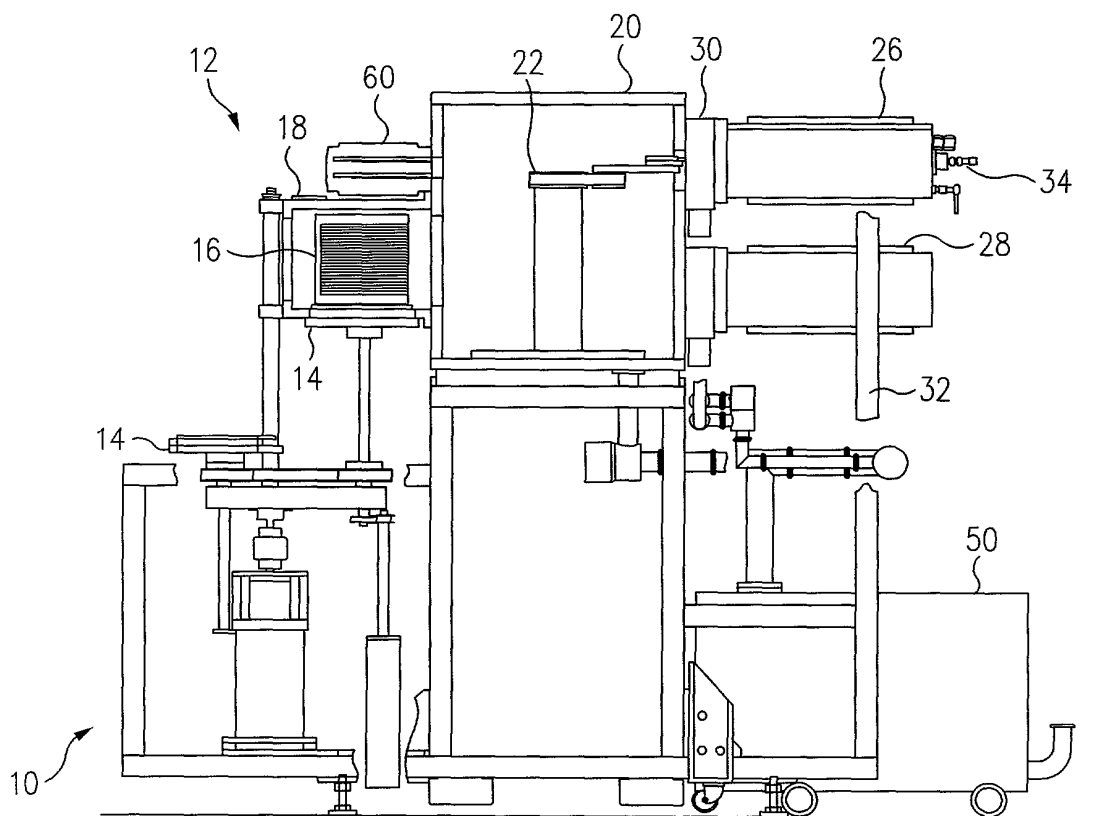


FIG. 2

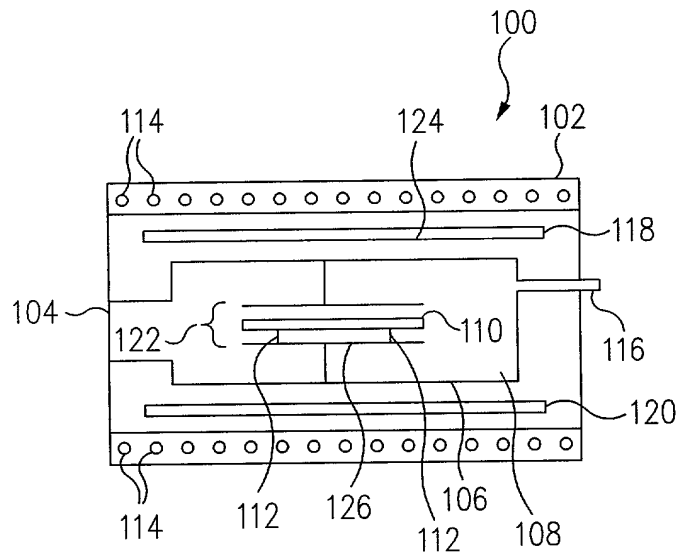


FIG. 3A

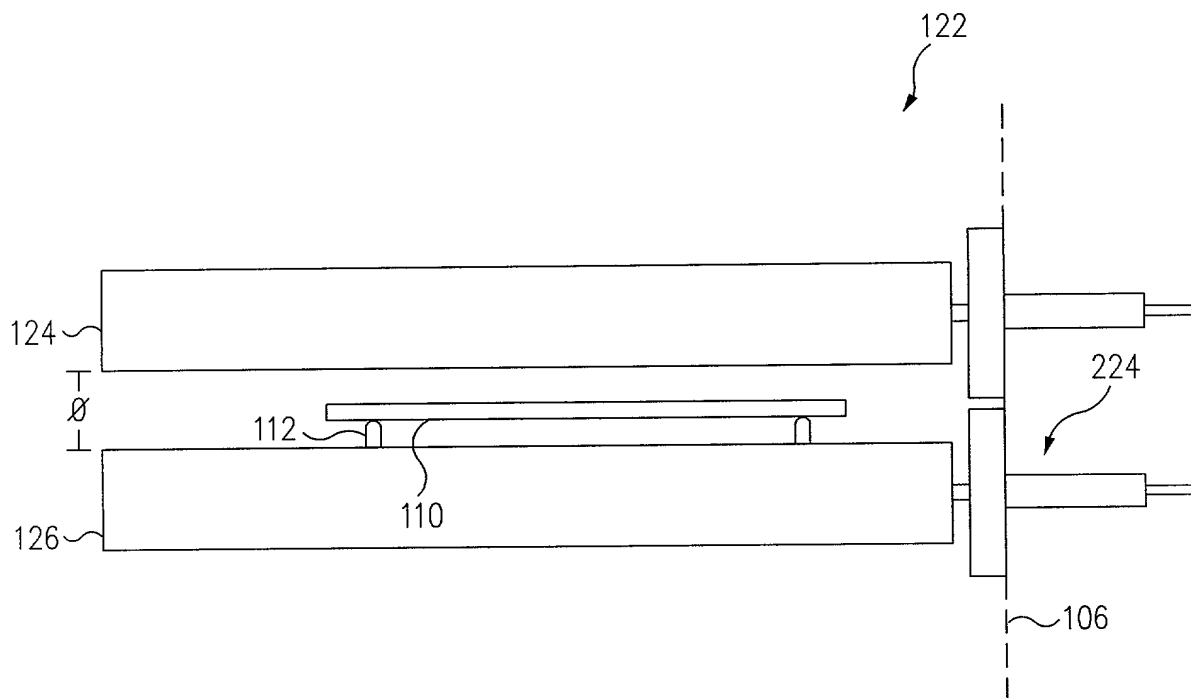


FIG. 3B

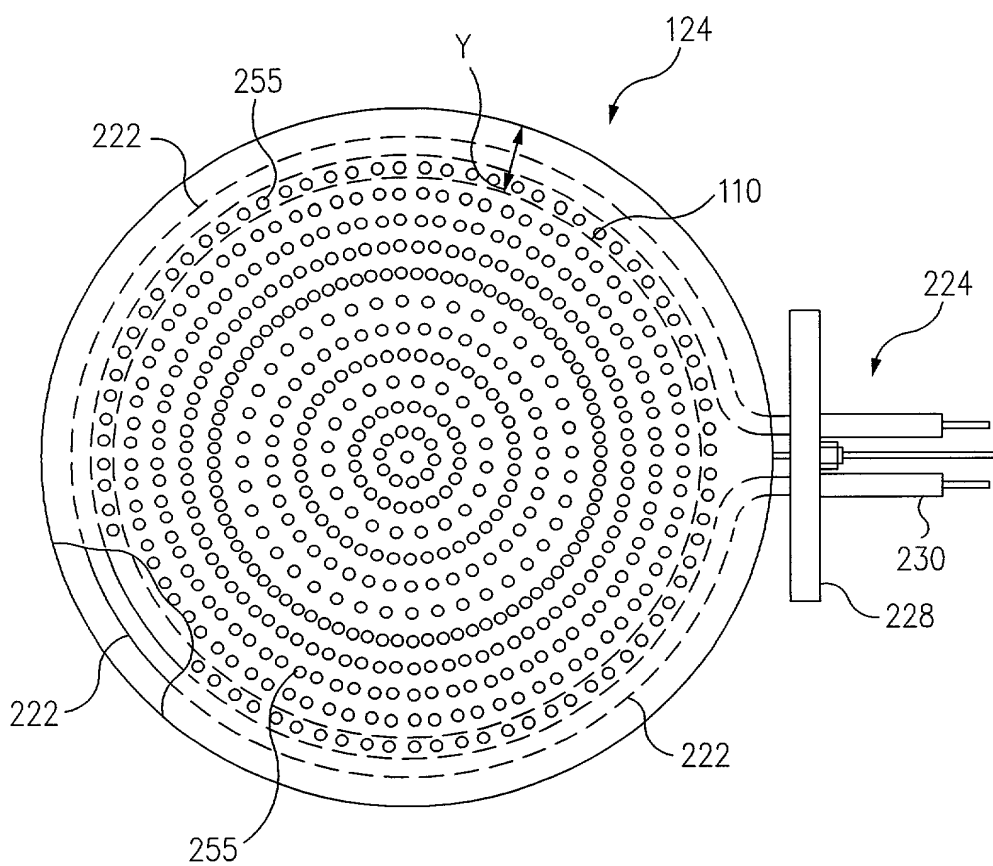


FIG. 4A

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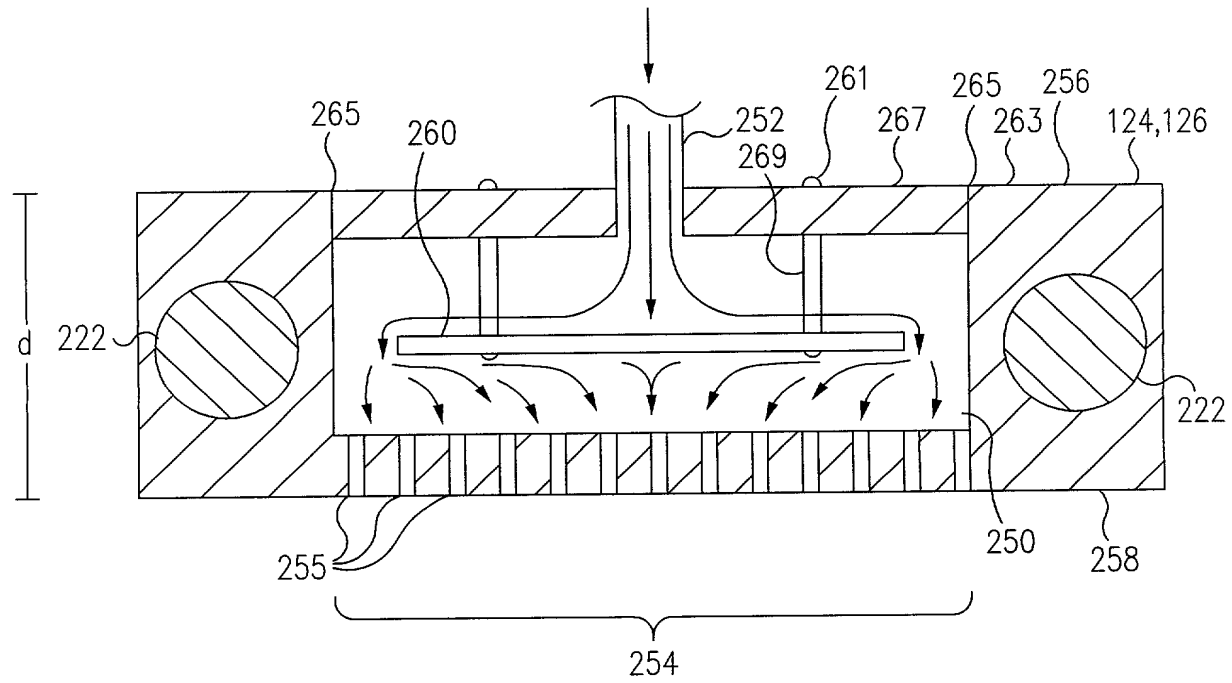


FIG. 4B

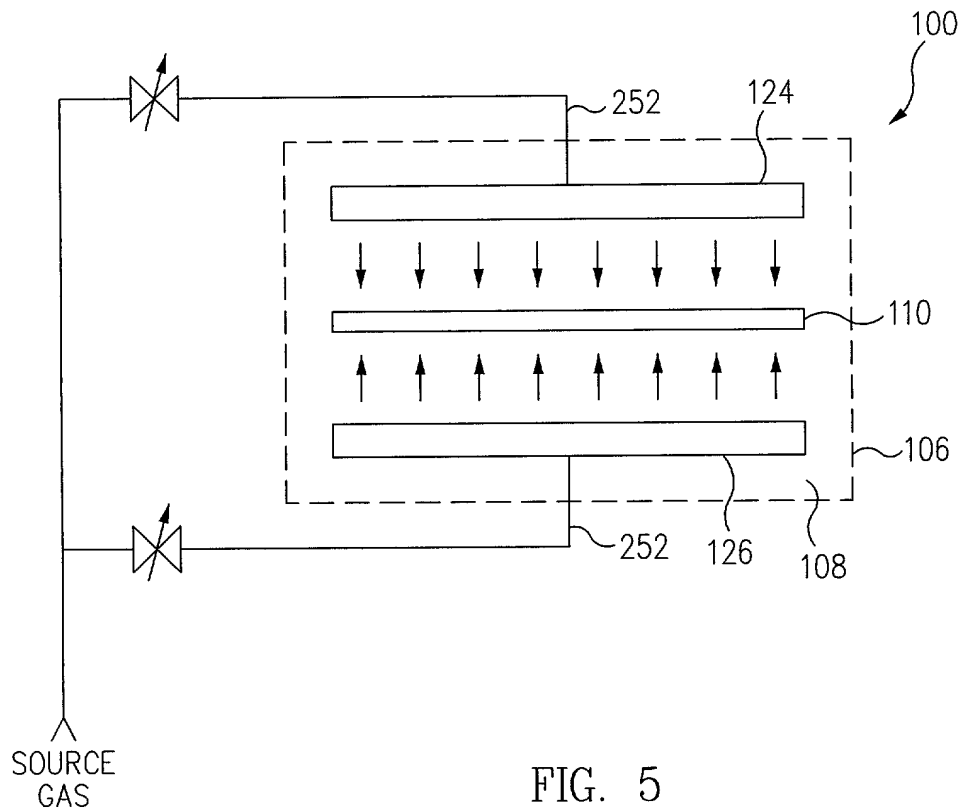


FIG. 5

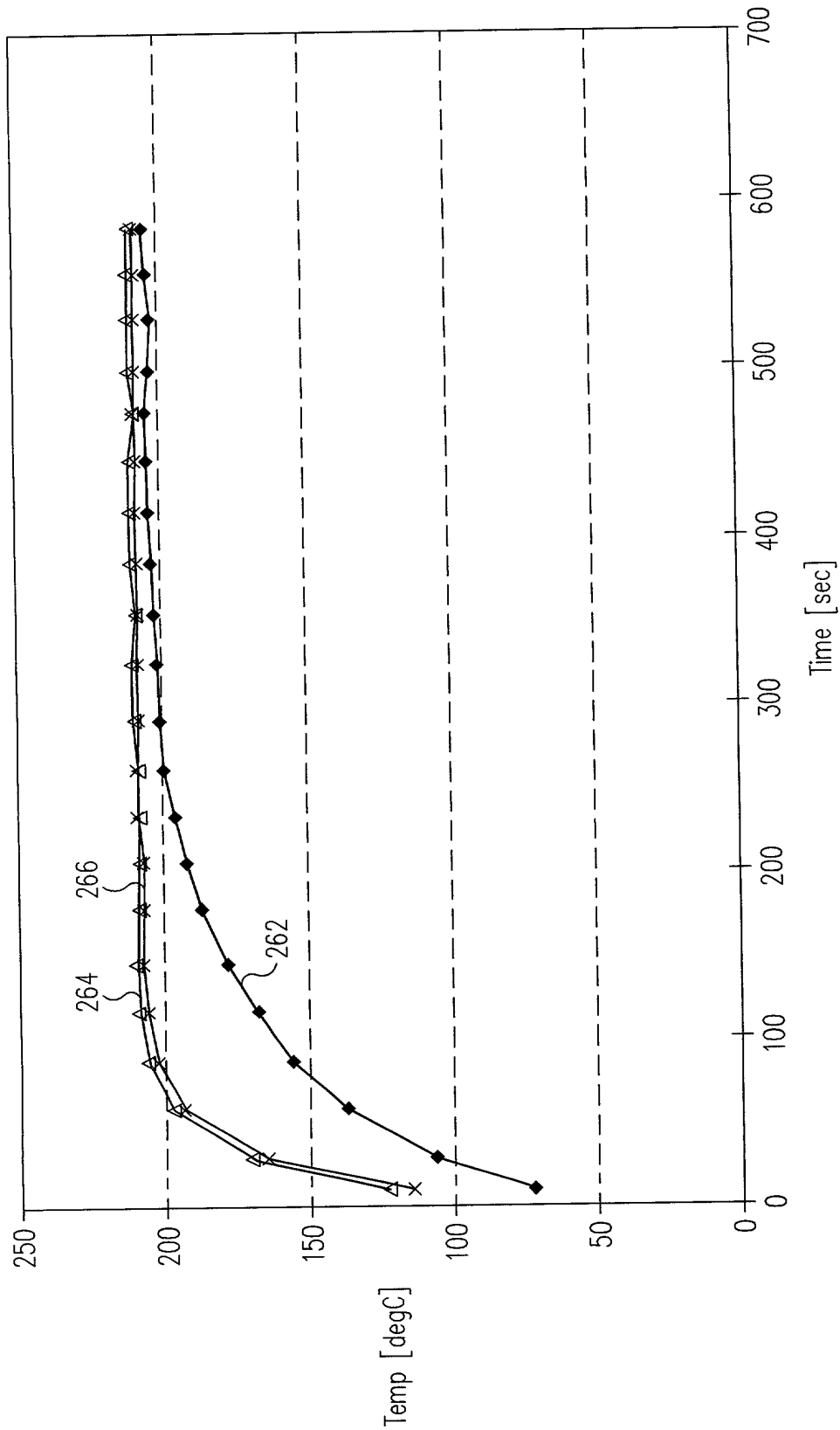


FIG. 6

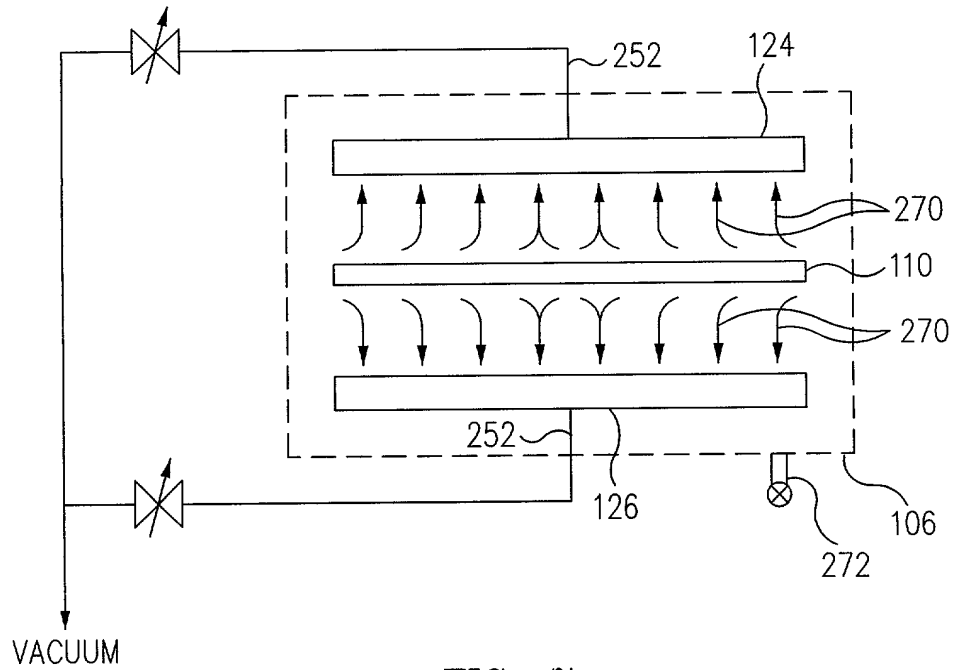


FIG. 7

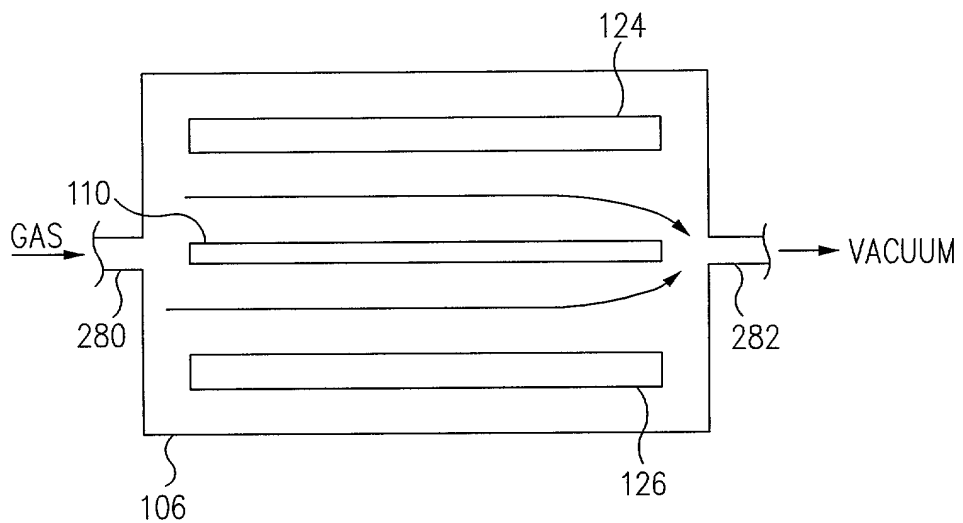


FIG. 8

DECLARATION FOR PATENT APPLICATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below adjacent to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of subject matter (process, machine, manufacture, or composition of matter, or an improvement thereof) which is claimed and for which a patent is sought by way of the application entitled

GAS-ASSISTED RAPID THERMAL PROCESSING

which (check) ☒ is attached hereto.
☐ and is amended by the Preliminary Amendment attached hereto.
☐ was filed on as Application Serial No.
☐ and was amended on (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information, which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) of any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed:

Prior Foreign Application(s)			Priority Claimed	
Number	Country	Day/Month/Year Filed	Yes	No
N/A			<input type="checkbox"/>	<input type="checkbox"/>

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:

Provisional Application Number	Filing Date
N/A	

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) or PCT international application(s) designating the United States of America listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior application(s) in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information, which is material to patentability as defined in

Title 37, Code of Federal Regulations, § 1.56, which became available between the filing date of the prior application(s) and the national or PCT international filing date of this application:

Application Serial No.	Filing Date	Status (patented, pending, abandoned)
N/A		

I hereby appoint the following practitioners to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith:

Customer Number 24251



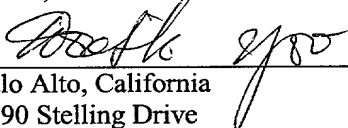
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 Facsimile: 408-453-7979

I declare that all statements made herein of my own knowledge are true, all statements made herein on information and belief are believed to be true, and all statements made herein are made with the knowledge that whoever, in any matter within the jurisdiction of the Patent and Trademark Office, knowingly and willfully falsifies, conceals, or covers up by any trick, scheme, or device a material fact, or makes any false, fictitious or fraudulent statements or representations, or makes or uses any false writing or document knowing the same to contain any false, fictitious or fraudulent statement or entry, shall be subject to the penalties including fine or imprisonment or both as set forth under 18 U.S.C. 1001, and that violations of this paragraph may jeopardize the validity of the application or this document, or the validity or enforceability of any patent, trademark registration, or certificate resulting therefrom.

Full name of sole (or first joint) inventor: Woo Sik Yoo

Inventor's Signature: 

Date: 11/7/01

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